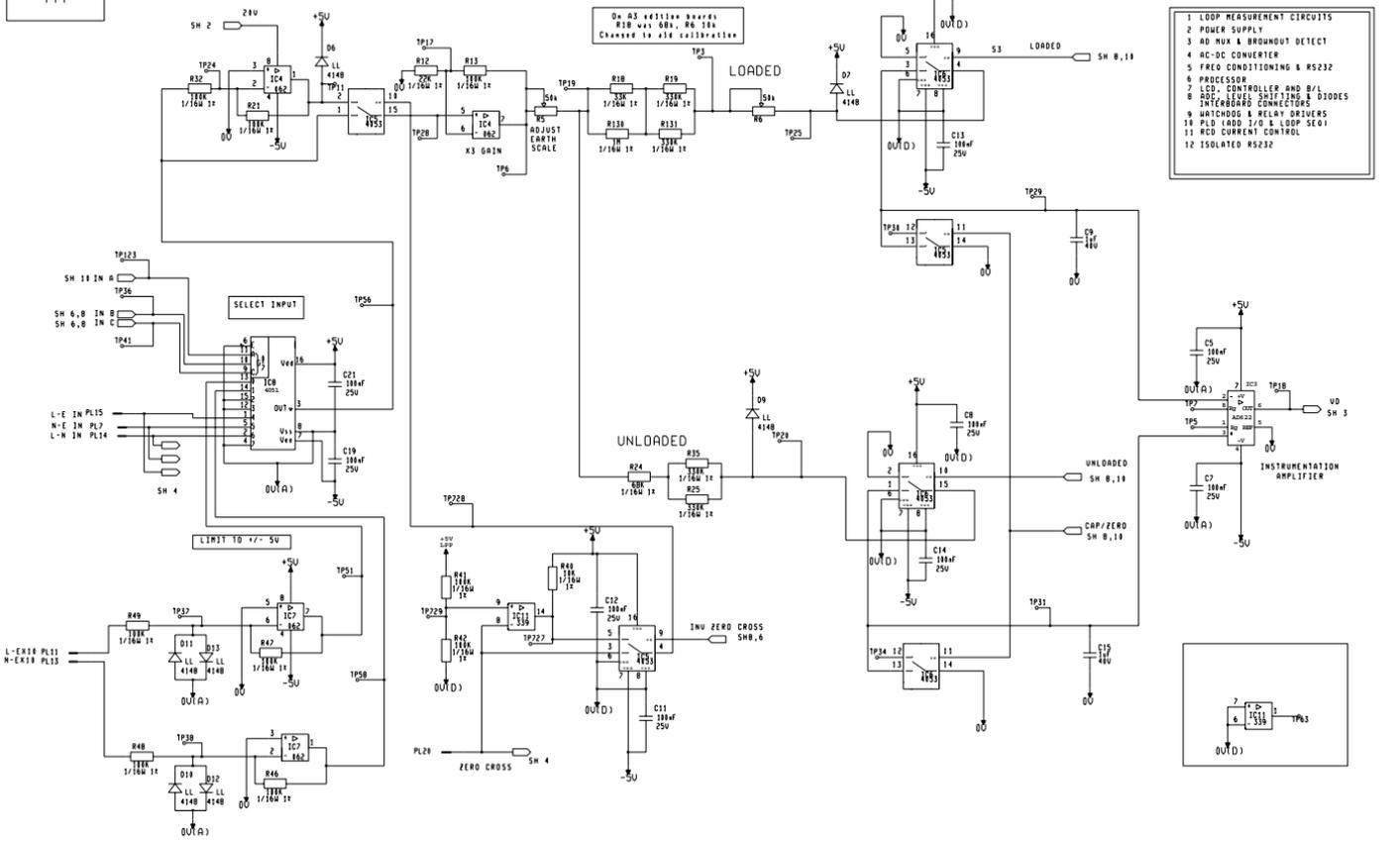


PSD AND AMP

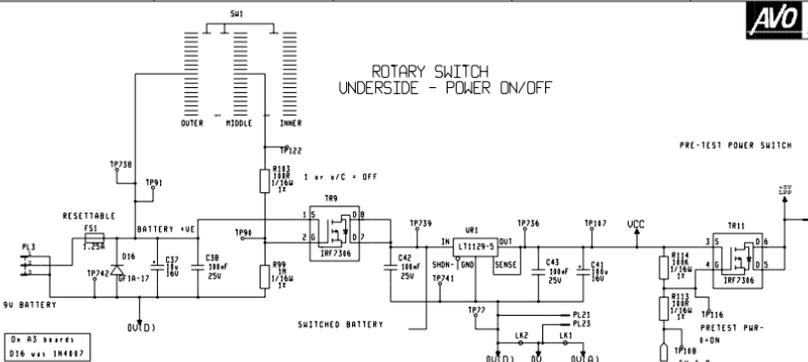
On A3 section boards  
R18 was 001, R6 104  
Changed to all calibration

- INDEX
- 1 LOOP MEASUREMENT CIRCUITS
  - 2 POWER SUPPLY
  - 3 AD NUX & BROWNOUT DETECT
  - 4 AC-DC CONVERTER
  - 5 PREC CONDITIONING & RS232
  - 6 PROCESSOR
  - 7 LCD CONTROLLER AND B/L
  - 8 SDC 12V5 REGULATING & DIODES
  - 9 INTERBOARD CONNECTORS
  - 10 SWITCHES & RELAY DRIVERS
  - 11 PLD (ADD I/O L LOOP SE01)
  - 12 ICD CURRENT CONTROL
  - 13 ISOLATED RS232



F1	F2	H3	H3A	H3	H3	H3H3H1	H3H1H3H3H5H5	H2	H3H3H3H3H5H5	H3H2	U5	U3	U5	U7	U5	U3H4	U1	U4	R4	H2	
TOOLING																					

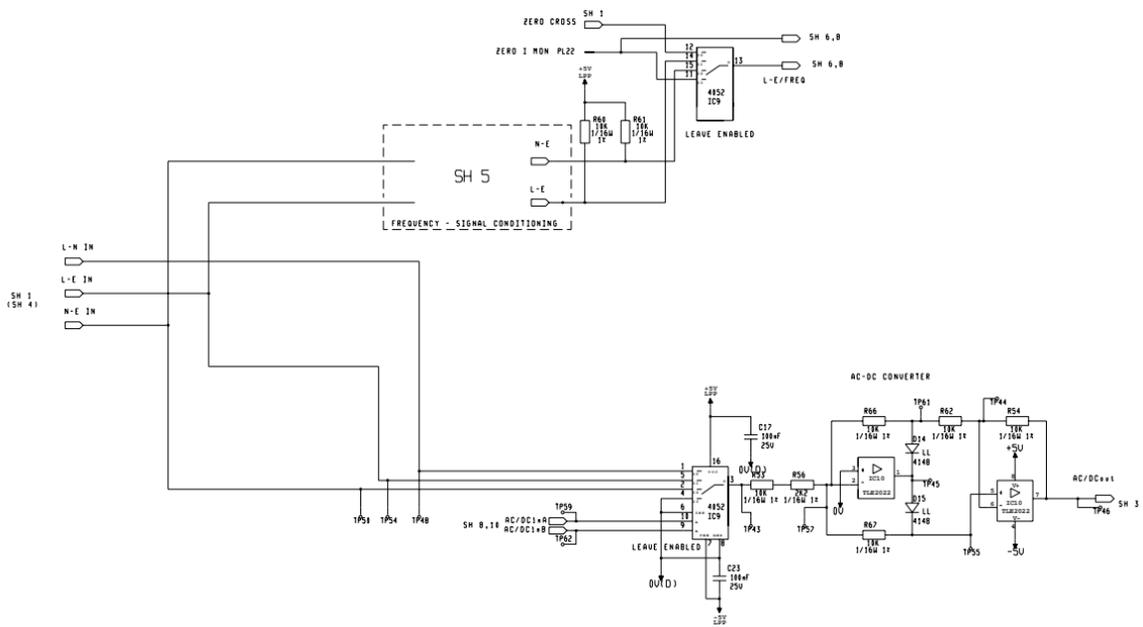
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	DATE: 3/5/82	CHK: JAC	CHK: DEC 98
	DRAWN: 3/5/82	CHK: JAC	CHK: DEC 98
	REV: 1 OF 12 SHEETS		



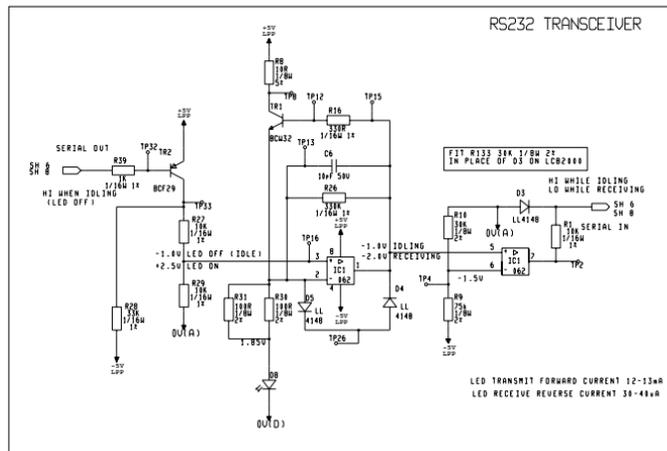
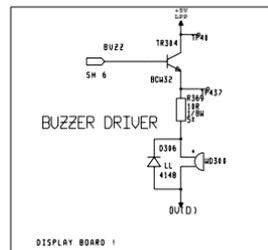
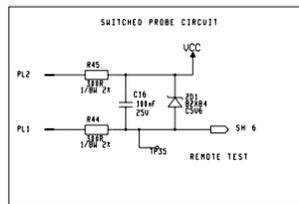
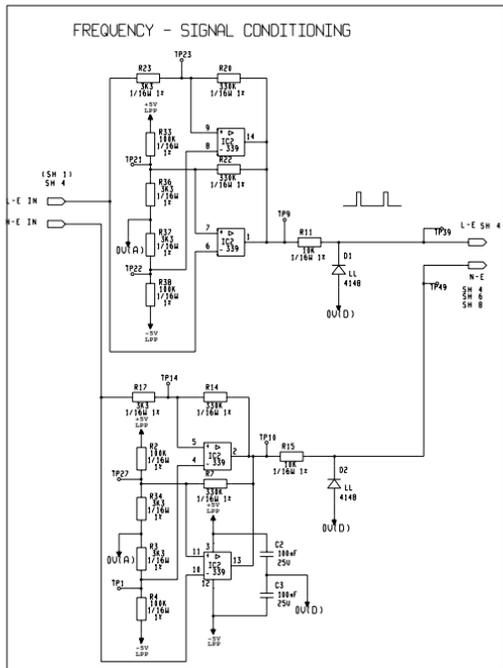


# MAIN PCB

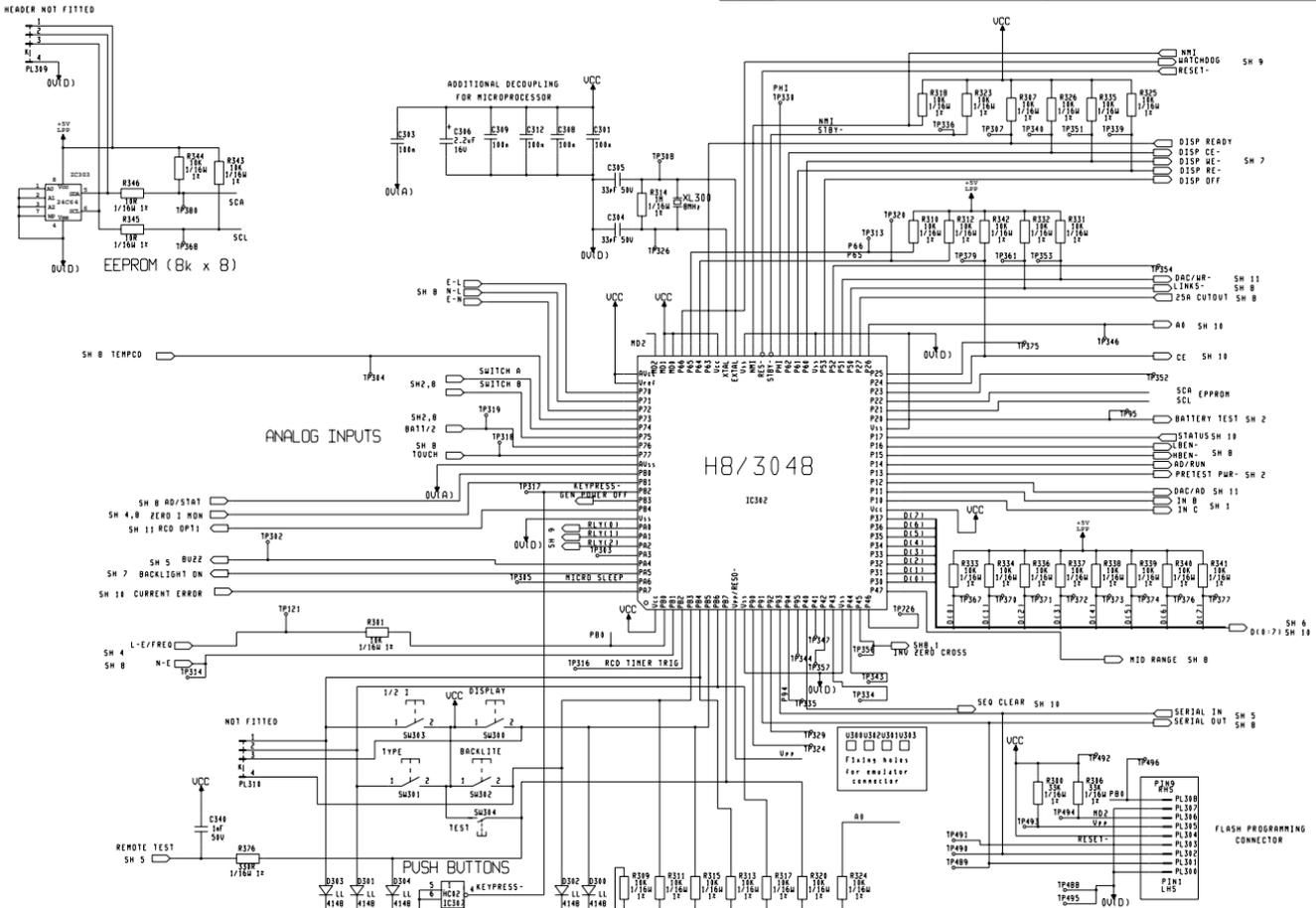
## VOLTAGE AND FREQUENCY - SIGNAL CONDITIONING



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	ISSUE	DATE	CHK BY	CND DRAWING, NO MANUAL CHANGES PERMISSIBLE			DRAWN: "E.S./E.F"		DATE: "F.A.V. '97"	
	3	7-5-92	23441				REV. 4		OF 12 SHEETS.	
	4	7/7/92	21178							



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	ISSUED	DRAWN	CHKD BY	CAD DRAWING	NO MANUAL CHANGES PERMISSIBLE	DESIGNED	DATE	CHKD DATE	INSTR.	DEC 98
	3	7-5-82	23442			4	27/9/82	21778		
	4								REV. 5 OF 12 SHEETS	



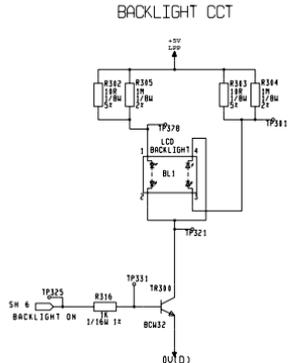
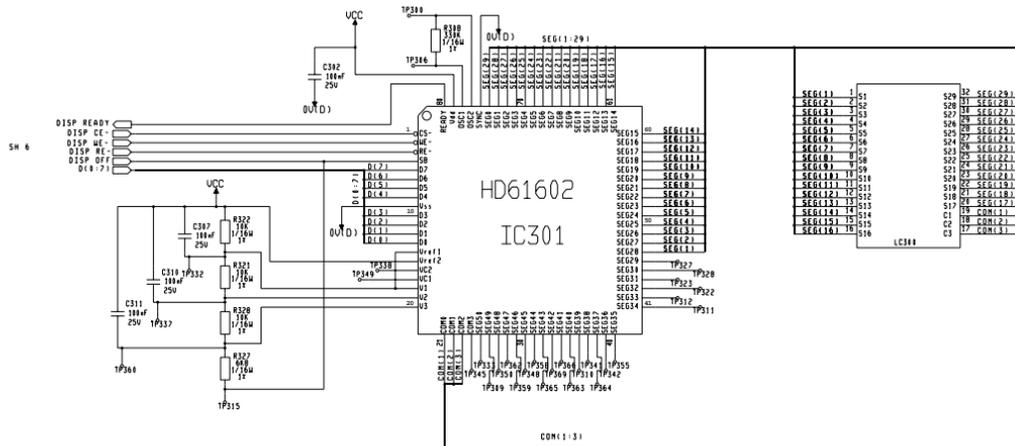
**MICROCONTROLLER CIRCUIT**

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IF MULTIPLE SHEET DRAWING, ALL SHEETS MUST BE OF THE SAME SIZE AND DRAWING, NO MANUAL CHANGES PERMISSIBLE

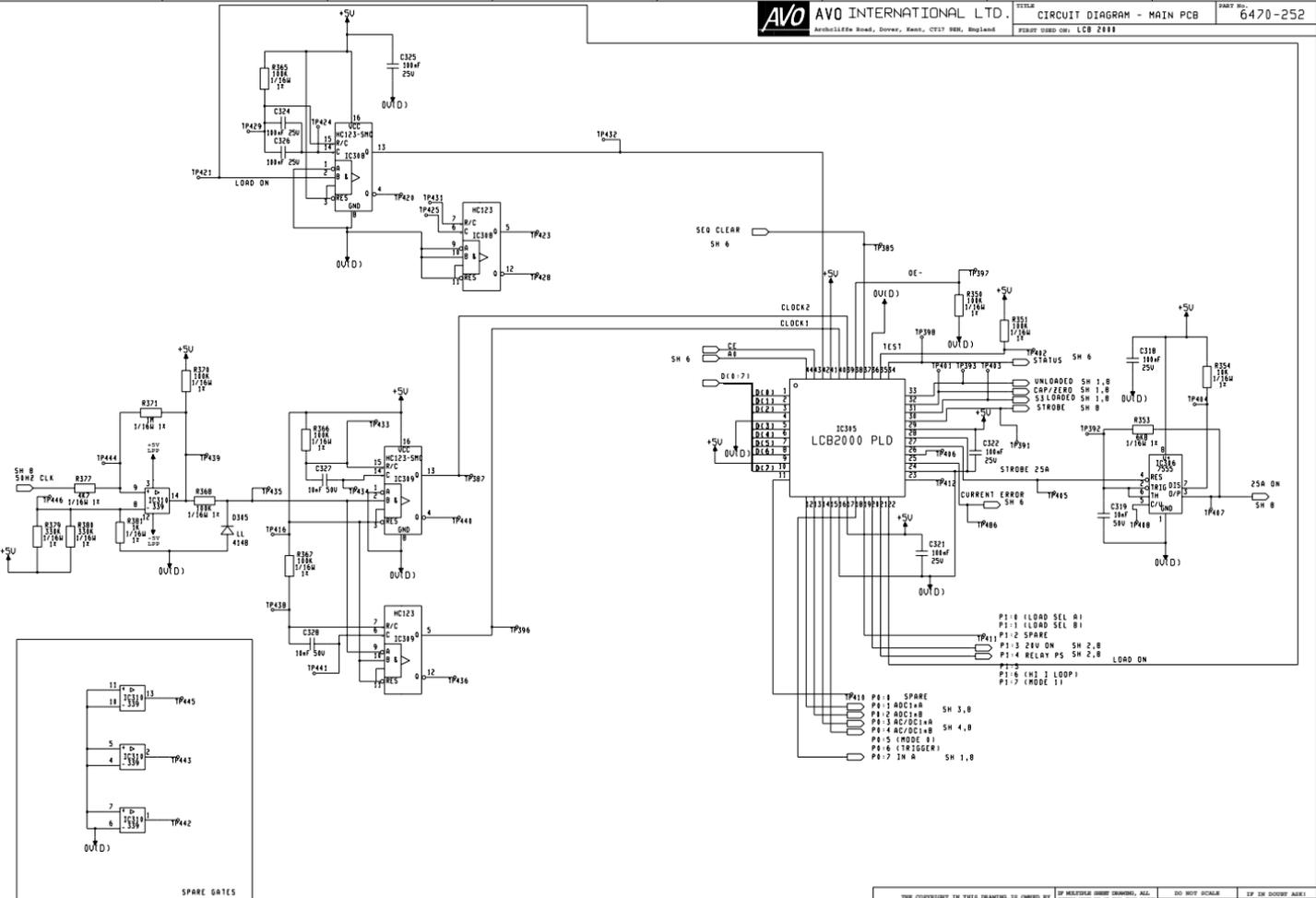
DESIGN	DATE	CHK BY	DO NOT SCALE	IF IN DOUBT ASK!
3	7-5-82	2341		
4	22-8-82	2178		

REV. 6 OF 12 SHEETS.









- P1:0 (LOAD SEL A)
- P1:1 (LOAD SEL B)
- P1:2 SPARE
- P1:3 24V DN SH 2,8
- P1:4 RELAY PS SH 2,8
- P1:5
- P1:6 (N1 J LOOP)
- P1:7 (MODE 1)
- P1:8 (LOAD SEL A)
- P1:9 (LOAD SEL B)
- P1:10 SPARE
- P1:11 ADD1:0 SH 5,8
- P1:12 ADD1:0
- P1:13 AC/DC1:0 SH 4,8
- P1:14 AC/DC1:0 SH 4,8
- P1:15 (MODE 0)
- P1:16 (1:8) (GEM)
- P1:17 IN A SH 1,8

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	CNO DRAWINGS, NO MANUAL CHANGES PERMISSIBLE.	DRAWN: CSK	CHECKED: DEC 98
ISSUE: 4	DATE: 17/12/21/98	DRAWN: DEC 98	INT: 18 ON 12 DIMS.

### RCD CIRCUIT CURRENT CONTROL

